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Nanette W. Combs

(Depositor's name)

Nanette Combs

(Signature)

5/4/2005

(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/688,550	10/17/2003	Jonggook Kim	P05779	1147

TITLE OF INVENTION: METHOD FOR QUANTIFYING SAFE OPERATING AREA FOR BIPOLAR JUNCTION TRANSISTOR

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$0	\$1400	07/06/2005
EXAMINER	ART UNIT		CLASS-SUBCLASS		
LB, JOHN H	2863		702-117000		
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(B) RESIDENCE: (CITY and STATE OR COUNTRY)

National Semiconductor Corporation

Santa Clara, California

Please check the appropriate assignee category or categories (will not be printed on the patent): Individual Corporation or other private group entity Government

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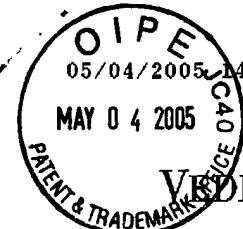
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U.S. Patent Application No. 10/688,550, filed October 17, 2003 Docket No. P05779
(11461.00.5779)

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Nanette Combs

Nanette W. Combs

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